

Notice of References Cited

Application/Control No.

10/015,350

Applicant(s)/Patent Under
Reexamination
GONZALEZ ET AL.

Examiner

Stephen E. Jones

Art Unit

2817

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,545,522	04-2003	Mughal et al.	327/334
	B	US-5,194,765	03-1993	Dunlop et al.	307/443
	C	US-5,459,440	10-1995	Claridge et al.	333/17.3
	D	US-6,486,612	11-2002	Fujimura et al.	315/225
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Hayt et al., Engineering Circuit Analysis, 1978, McGraw-Hill Book Company, 3rd Edition, page 68.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.